



Attorney Docket No. 1614.1074

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Hideo MIYAKE et al.

Application No.: 09/654,527

ATTENTION

Confirmation No.: 7021

CERTIFICATE OF CORRECTION

Filed: September 1, 2000

BRANCH

U.S. Patent No.: 7,401,204

Issued: July 15, 2008

For: SELECTIVE INSTRUCTION ISSUING PARALLEL PROCESSOR

Certificate
NOV 05 2008
of Correction

REQUEST FOR CERTIFICATE OF CORRECTION

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Sir:

Patentees respectfully request that a Certificate of Correction be issued in the subject patent, pursuant to 35 U.S.C. §254 and 37 C.F.R. §1.322, to correct the mistakes shown on the attached Form PTO-1050.

Since the mistakes are Patent Office mistakes, it is believed that no fee is required.

Respectfully submitted,

STAAS & HALSEY LLP

Date: Oct 13, 2008

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UNITED STATES PATENT AND TRADEMARK OFFICE

CERTIFICATE OF CORRECTION

PATENT NO : 7,401,204

DATED : July 15, 2008

INVENTOR(S) : Hideo MIYAKE et al.

First Page, Column 2, under the heading "Foreign Patent Documents", Line 4,
delete "JP 8-234978 9/1996" (Repeated Occurrence).

Column 26, Line 55, change "plurality of" to --N--.

Column 26, Lines 62-63, after "the" change "plurality of " to --N--.

Column 27, Line 4, change "plurality of" to --N--.

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PATENT NO. 7,401,204

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